

JUL 07 2003

**Complete if Known**

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| Application Number     | 09/514,674                  |
| Filing Date            | February 28, 2000           |
| First Named Inventor   | William H. Robertson et al. |
| Group Art Unit         | 2182                        |
| Examiner Name          | Stacy A. Whitmore           |
| Attorney Docket Number | 248/225                     |

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|-------|---|----|---|
| Sheet | 1 | of | 2 |
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| FOREIGN PATENT DOCUMENTS |                       |                         |                     |                                      |   |   |   |                |
|--------------------------|-----------------------|-------------------------|---------------------|--------------------------------------|---|---|---|----------------|
| Examiner Initials*       | Cite No. <sup>1</sup> | Foreign Patent Document |                     |                                      | Name of Patentee or Applicant of Cited Document | Date of Publication of Cited Document<br>MM-DD-YYYY | Pages, Columns, Lines, Where Relevant<br>Passages or Relevant<br>Figures Appear | T <sub>6</sub> |
|                          |                       | Office <sup>3</sup>     | Number <sup>4</sup> | Kind Code <sup>5</sup><br>(if known) |   |   |   |                |
| NW<br>↓                  | 5                     | EP                      | 0433066             |                                      | Hewlett Packard                                 | 06-1991   |   |                |
|                          | 6                     | EP                      | 0944002             |                                      | Sony Europe                                     | 09-1999   |   |                |
|                          | 7                     | EP                      | 1063599             |                                      | Dole  | 12-2000   |   |                |
|                          | 8                     | PCT                     | WO<br>01/65422      |                                      | Robertson et al.                                | 09-2001   |   |                |

**Examiner  
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# **SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 2 of 2

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## **OTHER DOCUMENTS -- NON PATENT LITERATURE**

| Examiner Initials * | Cite No. | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|---------------------|----------|---|----------------|
| <i>SW</i>           | 9        | Benini, L. et al., "Distributed EDA tool integration: the PPP paradigm," Proceedings of the International Conference on Computer Design, 10/1998, IEEE, pgs. 448-453  |                |
|                     | 10       | Blanchard, D., "EDA on the WWW PCB CAD," Printed Circuit Design, Miller Freeman, 02/1998, vol. 15, no. 2, pgs. 34-37  |                |
|                     | 11       | Chan et al., "WELD - An Environment for Web-Based Electronic Design," Proceedings of the 35 <sup>th</sup> Design Automation Conference, 05/1998, pgs. 146-151   |                |
|                     | 12       | Geppert, L., "The Rising Tide of Web-Based Tools and Services Augurs the Next Sea Change in Electronic Design IC Design on the World Wide Web," IEEE Spectrum, 06/1998, vol. 35, no. 6, pgs. 45-50  |                |
|                     | 13       | Moretti, "Got IP," Printed Circuit Design, Miller Freeman, 10/1998, vol. 15, no. 10, pgs. 31-33   |                |
|                     | 14       | Perrin, B., "Web-Based Circuit Engineering," Circuit Cellar Int, 03/1999, no. 104, pgs. 28-30, 33, 35, 37-38  |                |
|                     | 15       | Schindler, P. et al., "IP repository, a Web based IP reuse infrastructure," Custom Integrated Circuits, 03/1999, IEEE, pgs. 415-418   |                |
|                     | 16       | Konduri et al., "A Framework for Collaborative and Distributed Web-Based Design," Proceedings of the 36 <sup>th</sup> Design Automation Conference, 09/1999, pgs. 898-903   |                |
|                     | 17       | Wisey, P., "Web-Based Analysis and Distributed IP," Proceedings of the 1999 Winter Simulation Conference, pgs. 1445-1453  |                |
|                     | 18       | Rao et al., "Web-Based Network Analysis and Design," ACM Transactions on Modeling and Computer Simulation, 01/2000, vol. 10, issue 1, pgs. 15-38  |                |
| <i>W</i>            | 19       | Wen et al., "Concurrent-Simulation-Based Remote IP Evaluation over the Internet for System-On-Chip Design," 14 <sup>th</sup> International Symposium on System Synthesis, 09/2001, pgs. 233-238   |                |
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